Masked, anisotropic thermal etching and regrowth for in situ patterning of compound semiconductors

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To produce well-defined device structures having ultrasmall areas in compound semiconductors, it is necessary that lateral variations in doping and composition be realizable having interfacial quality comparable to that presently achieved vertically with techniques such as molecular beam epitaxy (MBE). This letter describes a novel scheme involving the controlled, patterned sublimation of GaAs, termed submicron lithography by molecular vaporization (SUBLIME), which is performed in situ within a MBE growth chamber. The strong orientation dependence of the removal rate from (100) substrates is discussed and final morphology is shown to be critically dependent on both substrate temperature and incident As flux. Several materials have been used successfully as patterned masks, and Al_{0.6}Ga_{0.4}As has been demonstrated to be a versatile SUBLIME “etch stop.” High-resolution micrographs of the resulting submicron structures are shown and initial results from studies of dopant redistribution and in situ regrowth after patterning are presented. Finally, some implications for future device fabrication are discussed.

In order to produce well-defined device structures in compound semiconductors with critical dimensions below 1 \( \mu \text{m} \), one must be able to vary material properties such as doping and alloy composition controllably. In addition, the interface between different regions must be of nearly perfect quality in terms of crystalline order and impurities. These requirements are now met routinely for planar (2D) structures by several epilayer techniques including molecular beam epitaxy (MBE) and liquid phase epitaxy (LPE). These techniques all have two features in common which are necessary for high-quality interfaces: first, they are relatively low-energy processes so that crystalline material near the interface is not disrupted by the growth process, and second, they are in situ techniques where the interface is never exposed to a “hostile” environment between the formation of successive layers.

The formation of corresponding one- and zero-dimensional (1D and 0D) structures would ideally involve 3D epitaxial growth, which is not expected to be technologically feasible in the near future. Focused ion beam (FIB) technology may make an impact here through growth-coincident doping, but throughput and resolution difficulties (particularly at low energies) are presently very discouraging. An obvious alternative is to grow a planar structure using conventional techniques, pattern said structure laterally via some etching technique, and epitaxially regrow subsequent layers on the now nonplanar substrate. This has been attempted with limited success on relatively large structures by performing a chemical etch as the second step and cleaning/polishing before regrowth by MBE or LPE. While the resulting epitaxy produces relatively good-quality material, the interface is not of sufficient quality for submicron structures with “active” regions in close proximity to that interface because of the necessary intermediate air exposure. And while reactive ion etching (RIE) is somewhat compatible with in situ patterning before regrowth, it is necessarily a high-energy process which results in considerable damage to the crystal near the interface. This letter describes the SUBLIME (submicron lithography by molecular vaporization) technique whereby patterning is performed in situ in an MBE growth chamber through a thermal process similar to MBE itself. The resulting structures reveal a strong etching anisotropy, good geometrical quality on a submicron scale, and excellent regrowth morphology by MBE.

The concept of removing GaAs thermally is not a new one—the volatility of both constituents and the interrelationship of partial pressures have been known for many years and were studied extensively by Cho and Arthur. More recently, reflection high-energy electron diffraction (RHEED) has been used to study the layer-by-layer growth and desorption of GaAs in MBE systems, and the observed temperature and flux dependences now appear to be well understood in terms of mass-action kinetics.7 The quasi in situ process of thermally removing a thin GaAs layer over Al_{0.6}Ga_{0.4}As after chemical etching and before regrowth has even been employed to produce 3-\( \mu \text{m} \)-wide double-heterostructure (DH) lasers.8 The work described here is a significant advancement, showing for the first time the submicron-scale structure that results from the anisotropic, thermal etching of masked (100) GaAs surfaces. The anisotropy is due to the activated nature of the process, and the removal rates and in situ environment are ideal for exploitation in submicron device fabrication.

In this work, the SUBLIME process has been used to pattern (100) GaAs substrates and MBE-grown heterostructures within a MBE chamber. The samples were In bonded to a nonrotating holder and subjected to a variety of time/temperature/As flux combinations, with the temperature calibrated by optical pyrometer. Figure 1 shows a scanning electron microscope (SEM) micrograph of a GaAs sample upon which a WSi_2 mask was patterned (1 \( \mu \text{m} \) lines and spaces, by 50 nm thick), prior to cleaning and introduction into the MBE growth chamber. The thermal etch

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FIG. 1. Cross section of SUBLIME-etched (100) GaAs sample after 1 h at 725 °C with \( P_{As} = 3.5 \times 10^{-6} \) Torr. Exposed faces are (111)A (Ga-terminated) crystal planes.

was executed under an As\(_3\) beam yielding an equivalent pressure of \( 3.5 \times 10^{-6} \) Torr, for 1 h at 725 °C. The temperature and \( P_{As} \) flux dependences we observe from 1-h etch times are consistent with RHEED observations\(^5\)\(^6\) that indicate an order of magnitude etch rate change for every 40 °C of substrate temperature in this range: at 675 °C the exposed GaAs surface is only weakly pitted, at 725 °C the etch rate is about 1 \( \mu \)m/h in the (100) direction and highly anisotropic, and at 765 °C the substrate etched so rapidly that no crystal facets appeared stable and 1 \( \mu \)m features were completely destroyed. The approximate etch rates observed here show an absolute temperature agreement within ≈ 5 °C of those previously reported from RHEED studies.\(^6\)

Figure 1 also illustrates the self-terminating nature of the crystallographically determined, anisotropic, "V" etch obtained with masking lines aligned parallel to the (011) direction; etching for 2 h increased the undercut by only about 100 nm. This anisotropy is due to the relative stability of the polar (111)A (Ga-terminated) planes which is also observed for chemical and photodarketching of GaAs. Similarly, the (111)B (As-terminated) face shows no stability to the SUBLIME process. In contrast to a chemical etch, however, the nonpolar (110) planes all appear to be stable at 725 °C, yielding a number of possible etched morphologies which depend on the relative pattern-crystal orientation. This was demonstrated by etching a variety of geometries into a MBE-grown heterostructure masked with Si\(_3\)N\(_4\). The starting heterostructure is shown in Fig. 2(a), consisting of a (100) GaAs substrate, 0.2 \( \mu \)m of undoped GaAs buffer, a 10-nm Al\(_{0.3}\)Ga\(_{0.7}\)As SUBLIME etch stop, and an upper 0.5-\( \mu \)m GaAs layer to be etched. The nitride mask was patterned to yield a series of gratings for cross-

FIG. 2. Views of MBE-grown heterostructure and SUBLIME patterning results: (a) sketch of starting heterostructure on a (100) GaAs substrate, (b) micrograph of etched pattern for mask lines in the (011) direction (same orientation as Fig. 1), (c) same as (b), but in the (011) direction, (d) top view, for lines in the (011) direction.
section studies and a variety of arbitrary shapes for top view of the resulting morphology. The $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$ etch stop was found to be fairly stable at 700 °C as long as the $\text{As}_2$ flux was kept above $7 \times 10^{-6}$ Torr, and results for (011) oriented nitride lines are shown in Fig. 2(b). The stability of the vertical (011) planes is illustrated by etching between nitride lines parallel to the (011) direction as shown in Fig. 2(c), while etching lines oriented at 45° [(011) direction] exposed the various angled (110) faces seen from the top in Fig. 2(d). The slight right-left anisotropies observed in the cross-section micrographs have been correlated with the incident beam direction and the etch-rate dependence upon $\text{As}_2$ flux, and should be eliminated with the use of a rotating substrate holder.

The $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$ etch-stop instability in the absence of sufficient $\text{As}_2$ flux, along with an $\text{As}_2$ beam shadowing produced by the combination of vertical sidewalls and slight undercutting shown in Fig. 2(b), has been used to produce some remarkable trench structures such as that shown in Fig. 3. This micrograph illustrates again the strong anisotropy of the SUBLIME process, and also demonstrates that thin $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$ layers may either be used as an etch stop or etched completely through using SUBLIME, while higher Al composition layers provide less versatility. The cleaned nitride lines are shown in Fig. 2(b). The stability of the resulting morphology. The $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$ etch stop adjacent to the patterned feature, and resulting trench formation.

The initial and regrown layers were Si doped, confirms that non-volatile dopants such as Si are not removed during SUBLIME; instead they pile up at the moving interface to produce an effective "delta doping." It is expected that this can be avoided, if desired, by using volatile dopants such as Mg. Further details concerning dopant redistribution and regrowth over patterned heterostructures such as those shown in Fig. 2 will be presented elsewhere. It should be noted, however, that the dopant redistribution is detected through the carriers produced, and that the observed accumulation (rather than depletion) indicates not only that the "piled-up" dopants are largely activated, but also that the interface is not "pinned" by a large density of interface states. The unpinned state of this interface is an important result if the SUBLIME process is to be used for device fabrication.

From initial results shown here, several general observations may be made. First, it is clear that the SUBLIME process fulfills the basic requirements for the formation of high-quality lateral interfaces necessary for good ultrasmall devices: it is both an in situ and low-energy patterning technique. It is also obvious that this process should be extendable to other volatile compound semiconductors such as $\text{In}_x\text{Ga}_{1-x}\text{As}$. And while it has not yet been determined what the ultimate interface quality can be using SUBLIME, results presented here suggest that virtually all GaAs-based devices could benefit from application of this technique.